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## PATENT ABSTRACTS OF JAPAN

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### (54) IMAGING APPARATUS, CAMERA AND INFORMATION PROCESSOR

(57) Abstract:

PROBLEM TO BE SOLVED: To reduce the noise due to a shift operation of an imager and also its power consumption.

SOLUTION: A horizontal scanning circuit 150 has a shift register divided into a plurality of sub-shift registers 151-153. Shift clock control circuits 154-156 control the supply of the shift clocks to the sub-shift registers 151-153 one by one.

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